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	Application No.	Applicant(s)		
	10/077,164	MIYAHARA, YASUMITS	MIVAHADA VASIIMITSII	
Notice of Allowability	Examiner	Art Unit	<u>,,, </u>	
	Naheed Ejaz	2631		
The MAILING DATE of this communication application application application application and the second se	S (OR REMAINS) CLOSED ir 5) or other appropriate commo RIGHTS. This application is s	n this application. If not included unication will be mailed in due cou	rse. THIS	
1. X This communication is responsive to <u>09 September 2005</u>	<u>5</u> .			
2. The allowed claim(s) is/are 1-13.				
 3.	ve been received. ve been received in Application documents have been received in Application documents have been received in Application in the header according to 37 CF posit of BIOLOGICAL MAT	on No In this national stage application In a reply complying with the require AMINER'S AMENDMENT or NOTE In declaration is deficient. In the Office action of the drawings in the front (not the backer) FR 1.121(d). ERIAL must be submitted. Note	ements	
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SE Paper No./Mail Date	6. Interview S Paper No. 3/08), 7. Examiner's	oformal Patent Application (PTO-15) Summary (PTO-413), /Mail Date <u>11/08/2005</u> . Amendment/Comment Statement of Reasons for Allowa		
of Biological Material	9. 🔲 Other	-: \		

TESFALDET/BOCKIRE
PRIMARY TXAMINER

Part of Paper No./Mail Date 20051108

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Norman P. Soloway, Reg.No.24, 315 on 11/08/2005.

The application has been amended as follows:

In the claims:

Claim 1, line 10, after "digital filter"--- a period measuring circuit for generating measurement of period changes in the sign signal output by said digital filter; a threshold holding circuit for setting a threshold equal to a period of an intermediate frequency between the normal frequency band and the aliasing frequency band; a comparator for comparing and determining whether or not the period measured by said period measuring circuit is larger than the threshold, and for outputting a shift control signal when it is determined that the measurement of said period measuring circuit is not larger than the threshold; and a shift register for shifting a signal which is input from said digital filter and stored, based on said shift control signal, and for suppressing an amplitude of the aliasing noise--- has been added.

Claim 3 has been deleted.

Claim 4, line 1, "claim 3" has been changed to read as ---claim 1---.

Claim 5, line 1, "claim 3" has been changed to read as ---claim 1---.

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Claim 6, line 3, after "comprises:"---a period measuring circuit for measuring a changing period of the sign signal which is outputted by said digital filter; a threshold holding circuit for holding a period of an intermediate frequency between the normal frequency band and the aliasing frequency band; a comparator for comparing and determining whether or not the period measure by said period measuring circuit is larger than the threshold set by said threshold holding circuit and outputting a clear signal when it is determined that the period is not larger than the threshold set by said threshold holding circuit; and---has been deleted.

Claim 8, line 5, after "sign signal"---a period measuring circuit for measuring a changing period of the sign signal outputted by said digital filter; a threshold holding circuit for holding a period of an intermediate frequency between the normal band and the aliasing band; a comparator for comparing and determining whether or not the period measured by said period measuring circuit is larger than the threshold which is set to said threshold holding circuit and for outputting a shift control signal when it is determined that the period measured by said period measuring circuit is not larger than the threshold; and a shift register for shifting a signal which is inputted from said digital filter and is stored, based on said shift control signal, and for suppressing an amplitude of the aliasing noise---has been added.

Claim 10 has been deleted.

Claim 11, line 1, "claim 10" has been changed to read as ---claim 8---.

Claim 12, line 1, "claim 10" has been changed to read as ---claim 8---.

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Claim 13, line 2, after "comprises:"---a period measuring circuit for measuring a changing period of the sign signal which is outputted by said digital filter; a threshold holding circuit for holding a period of an intermediate frequency between a normal band and an aliasing band; a comparator for comparing and determining whether or not the period measured by said period measuring circuit is larger than the threshold set to said threshold holding circuit and for outputting a clear signal when it is determined that the period is not larger than the threshold; and"--- has been deleted.

Remarks:

The above Examiner's Amendment was made in order to the following:

With respect to claims 1 and 3, claim 3 is being incorporated with claim 1 since claim 3 limitations specifically claimed the invention that is anti-aliasing circuit.

With respect to claims 8 and 10, claim 10 is being incorporated with claim 8 since claim 10 limitations specifically claimed the invention that is anti-aliasing circuit.

With respect to claims 4 and 5, in order to change the dependency of claims 4 and 5 from claim 3 to claim 1.

With respect to claims 11 and 12, in order to change the dependency of claims
11 and 12 from claim 10 to claim 8.

With respect to claims 6 and 13, in order not to have redundancy of the claimed limitations partial claims have been deleted.

2. The following is an examiner's statement of reasons for allowance:

The claimed subject matter is allowable because arts of record fail to teach or fairly suggest the claimed anti-aliasing circuit of claim 1 comprises:

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"an anti-aliasing circuit for suppressing or removing noise having an aliasing band, which is caused by the half-band processing in said digital filter, by using a sign signal outputted from said digital filter. a period measuring circuit for measuring a changing period of the sign signal outputted by said digital filter; a threshold holding circuit for holding a period of an intermediate frequency between the normal band and the aliasing band; a comparator for comparing and determining whether or not the period measured by said period measuring circuit is larger than the threshold which is set to said threshold holding circuit and for outputting a shift control signal when it is determined that the period measured by said period measuring circuit is not larger than the threshold; and a shift register for shifting a signal which is inputted from said digital filter and is stored, based on said shift control signal, and for suppressing an amplitude of the aliasing noise".

When used in combination with the following components as claimed by the applicant:

-A/D converter

-digital filter

Hence claim 1 is allowable.

Claims 2, 4-6 are allowable as being dependent from an allowable independent claim.

Claim 7 is allowable for the same reason given above in claim 1.

Claim 8 is allowable for the same reason given above in claim 1.

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Claims 9, 11-13 are allowable as being dependent from an allowable independent claim.

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

- 4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
 - -Stormont et al. (4,992,736) disclose radio frequency receiver for a NMR instrument.
 - -McGill et al. (4,603,703) disclose A/D converter, digital filter with polarity SW, detection and matching threshold.

Contact Information

- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Naheed Ejaz whose telephone number is 571-272-5947. The examiner can normally be reached on Monday Friday 8:00 4:30.
- 6. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on 571-272-3021. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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7. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Naheed Ejaz Examiner Art Unit 2631

11/10/2005

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PRIMARY EXAMINER